

ABSTRACT OF THE DISCLOSURE

Record controlling data received by an interface unit is transferred to a change controlling block, and transferred to a head analyzing block so that analysis of its head can be performed. In case the data following the head is a command, it is stored in a command storing register, and in case of compressed recording data, it is transferred to a data transfer controlling block. A MPU accesses the command storing register to analyze the command. The compressed recording data is stored in a FIFO memory via a first dedicated bus from the data transfer controlling block, and transferred to a DECU via a second dedicated bus.